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1. 打开界面。



2. 找到 core 所在文件夹:



 进入每个 core,为各个 core 进行编程(也可以从外部导入.c 文件)(我选用的是 0, 1,6,7,这四个 core 都在一个 cluster 中)

					//*****	********	*******	**********	*******	***
					//memory	y commun	ication			
					//*****	******	*******	******	*******	***
21#define I	FLAG_1 *(volatile	int*)	0x00001050		#define	FLAG_1	*(volat	ile int*)	0x0	0007050
22 #define 1	FLAG_2 *(volatile	int*)	0x00001150		#define	FLAG 2	*(volat	ile int*)	0x0	0007150
23#define I	FLAG_3 *(volatile	int*)	0x00001250		#define	FLAG 3	*(volat	ile int*)	0x0	0007250
24#define 1	FLAG_4 *(volatile	int*)	0x00001350		#dofine	FLAC 4	*(welat	ile int*)	010	0007250
25 #define d	denote_1 *(volatile int*)	0x	00001450	#deline	FLAG_4	"(VOIat	me mu.)	UXU	0007330
20#define d	ienote_2 "(volatile int")	UX OX	00001550	#define	denote_	1	*(volatile	int*)	0x00007450
28 #define	denote_5 *(volatile int*)	0x	00001750	#define	denote_	2	*(volatile	int*)	0x00007550
29	ichore_i (volucite inc)		00001100	#define	denote_	3	*(volatile	int*)	0x00007650
30 //*******	************	***********	*********	*******	#define	denote	4	*(volatile	int*)	0x00007750
31//main fu	unction								Sectore Sec	
32 //******	********	*******	*********	****	1/*****	******	******	*****	******	*****
33 int main(()				//main	function				
391	denote 1=0 denote	2=0 denote 3=0	denote 4=	0.	/////////			********		*****
36	int i=1.sum:	r o,uchote_b	,ucnote_1			()				
37 1	FLAG_1=0;				int mail	n()				
38 1	for(i=1;i<=2500;i+	+)			{					
39	FLAG_1+=i;					int i=1	;			
40	$//_flag_2NxN = 655$	35;				FLAG_2=	0;			
41 0	chile(1)					for(i=2	501;i<=5	000;i++)		
43	if(denote	1==1&&denote 2	==1&&denot	e 3==1&&denote 4==1) break		CONSIGNATION OF	FLAG 2+	=i:		
44 .	sum=FLAG_1+FLAG_2+	FLAG_3+FLAG_4:	i	c_s - induction c_i - i) bi can		denote	2=1 .	-,		
45 6	echol=sum;					weturn	· · ·			
46 1	return 0;				2	return	U,			
47 }					3					
18 //****	******	******	******	****	18 / /****	*******	*******	******	*******	***
19//memo:	ry communication	on			19 //memo	ry commu	nication			
20 / /****	*****	*******	*****	*****	20 / /****	******	*******	******	*******	***
21#defin	e FLAG_1 *(vol	latile int*)	0x00005050	21#defin	e FLAG_1	*(volat	tile int*)	0x0	0003050
22 #defin	e FLAG 2 *(vo	latile int*)	0x00005150	22 #defin	e FLAG 2	*(volat	tile int*)	0x0	0003150
23 #defin	e FLAG 3 *(vo	latile int*	5	0x00005250	23 #defin	FLAG 3	*(volat	tile int*)	0.00	0003250
24 #dofin	ELAC 4 *(vo	latilo int*		0x00005250	24 #dofin	ELAC A	*(volat	tilo int*)	010	0003250
25 #J-61	e FLAG_4 (VO.	*(l-t	ile inte	0x00003330	25 #J-fin	e FLAG_4	1 (VUIA)	*(]-+i]-	int*)	0003330
25 #defin	e denote_1	"(Volat	ile int-	0x00005450	25 #defin	e denote	-1	*(volatile	int")	0x00003450
26#defin	e denote_2	*(volat	ile int*	0x00005550	26 #defin	e denote	_2	*(volatile	int*)	0x00003550
27 #defin	e denote_3	*(volat	ile int*	0x00005650	27 #defin	e denote.	_3	*(volatile	int*)	0x00003650
28#defin	e denote_4	*(volat	ile int*	0x00005750	28 #defin	e denote	_4	*(volatile	int*)	0x00003750
29					29					
30 / /****	*****	******	*****	******	30 / /****	*******	******	*****	*****	*****
31//main	function				31//main	function	n			
32 / /****	******	******	******	******	32 //****	*******	*******	******	*******	*****
33 int ma	in()				33 int ma	in()				
34 {					34 {					
25	int is				25	int it				
20	THE I,				20	TILL 1;	0			
30	FLAG_3=0;				30	rLAG_4	=0;			
3/	Ior(1=5001;1-	<=/500;1++)			3/	for(i=	/501;1<=	10000;1++)		
38	FLAG.	_3+=i;			38		FLAG_4-	+=1;		
39	<pre>denote_3=1;</pre>				39	denote.	_4=1;			
40	return 0:				40	return	0:			
41 }					41}		1000			

4. 编好程序后,打开 telnet,或者直接在每个 core 文件夹内双击 run_script 程序(缺点是 无法 debug)



5. 在 telnet 中输入一个核中.c 文件的路径,格式为 "cd+空格+路径",回车

$\boxed{\mathbf{v}}$						Terminal				— • ×
<u>F</u> ile	<u>E</u> dit	<u>V</u> iew	<u>T</u> erminal	Ta <u>b</u> s	<u>H</u> elp					
Edit V dell: hevc	model eriuse r900b: _32/me	sim.i r = \$ /net/ /core	ni to in MG_LIB/m dellr900 01∎	clude ti_mod b/expo	the li elsim_ rt/ybf	ine : _verilog/l fan>[41]cd	ibmgmm.so /net/del	lr900b/ex	port/ybfan/	'jyz/me_

6. 在此路径下输入"source ./run_script",回车。

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Edit V dell hevc dell scri	model eriuse r900b: _32/me r900b: pt	lsim.i er = \$:/net/ e/core :/net/	ni to in MG_LIB/m dellr900 01 dellr900	clude ti_mod b/expo b/expo	the li lelsim_ rt/ybf rt/ybf	ine : _verilog/l fan>[41]cd fan/jyz/me	ibmgmm. /net/d _hevc_3	so ellr900 2/me/co	b/expo re01>[rt/ybfa 42]sour	m/jyz	/me_ run_	•
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7. core01 文件夹内就会生成两个.dat 文件(一个是储存 instruction,另一个是储存 data, 储存 data 的文件可能根据.c 文件的不同而不生成),对另外三个 core 中的.c 文件都进 行一遍这样的操作



8. 把所有的.dat 文件都拷入 soft 文件夹中



9. 在 me_hevc_32 路径下用 telnet 运行"perl makeone_all_in_one_ver2.pl soft",回车。



10. 在 me_hevc_32 中会生成 input.dat 文件

	File Browser: me_hev	rc_32	-0
<u>Fi</u> le <u>E</u> dit <u>V</u> iew <u>G</u> o <u>B</u> ookma	rks <u>H</u> elp		
Back Forward	Up Stop Reload	Dome Computer	
Location: /net/dellr900b/export/	ybfan/jyz/me_hevc_32	🥄 10	0% 🔍 🛛 View as List 👌
Γree ∨	× Name ~	Size Type	Date Modified
Videos	rerdiLog	9 items folder	Wed Apr 24 18:44:21 201
▷ 📁 byf	core00.txt	12 bytes plain text document	Thu Jul 18 15:30:31 2013
▷ 📁 hll ▽ 🧖 iur	dma0.txt	0 bytes plain text document	Wed Jul 31 14:30:32 201
V pyz	/ / dmal.txt	0 bytes plain text document	Wed Jul 31 14:30:32 201
INCA_libs	input.dat	2.5 KB MPEG video	Wed Jul 31 14:54:24 201
Com_config	 // makeone_all_in_one_ver2.pl	6.9 KB Perl script	Wed Mar 21 18:59:56 20
Config	multi_core_test.v	10.3 KB plain text document	Wed Jul 31 14:30:20 201
▷ 📁 dma_test	ncverilog.key	5 bytes plain text document	Wed Jul 31 14:25:27 201
▷ 📁 log	门 ncverilog.log	44.6 KB application log	Wed Jul 31 14:31:50 201
▷ □ lyh	🤠 offchip_mem.dat	1.3 MB MPEG video	Wed Jul 17 12:49:03 201
> Core00	🤠 offchip_mem_bak.dat	25.0 KB MPEG video	Fri Jul 5 19:29:00 2013
core01	门 one_lcu.log	622 bytes application log	Tue Jul 2 23:30:19 2013
▷ 📁 core04	门 run.f	7.6 KB Fortran source code	Wed Apr 24 18:43:34 201
Core05	j sim.bat	76 bytes plain text document	Wed Apr 24 18:43:35 201
▷ 📁 core06	₩ €	111	•

					Terminal	
<u>F</u> ile	<u>E</u> dit	<u>V</u> iew	<u>T</u> erminal	Ta <u>b</u> s	<u>H</u> elp	
Edit Vi dell: hevc. dell: _ver: dell:	model eriuse r900b: _32 r900b: 2.pl s r900b:	vjew sim.i r = \$ /net/ oft /net/		clude ti_mod b/expc b/expc	<pre>thep the line : delsim_verilog/libmgmm.so ort/ybfan>[41]cd /net/dellr900b/export/ybfan/jyz/me_ ort/ybfan/jyz/me_hevc_32>[42]perl makeone_all_in_on ort/ybfan/jyz/me_hevc_32>[43]wc input.dat</pre>	• e
						*

查看指令数,值为279

						Termina	վ)*)
<u>F</u> ile	<u>E</u> dit	<u>V</u> iew	<u>T</u> erminal	Ta <u>b</u> s	<u>H</u> elp								
Edit V dell hevc dell _ver dell 279 dell	mode: eriuse r900b _32 r900b 2.pl s r900b 279 r900b	<pre>lsim.i er = \$:/net/ soft :/net/ 2511 :/net/</pre>		ta <u>u</u> s clude ti_mod b/expo b/expo t b/expo	the line elsim_ rt/ybf rt/ybf rt/ybf	ne : verilog an>[41] an/jyz/ an/jyz/	;/libmgm cd /net 'me_hevc 'me_hevc	m.so /dellr9 _32>[42 _32>[43 _32>[44	00b/e:]perl]wc in]	xport/ makeo nput.d	'ybfan/ ne_all lat	jyz/me _in_on	e
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12. 打开 testbench

	File Browser: me_hev	c_32	
<u>File E</u> dit <u>V</u> iew <u>G</u> o <u>B</u> ookmarks	<u>H</u> elp		
Back Forward Up	Stop Reload Ho	me Computer	
Location: /net/dellr900b/export/ybfa	n/jyz/me_hevc_32	🔍 10	0% 🍳 🛛 View as List 📑
Tree 🛩 🗙 🗙	Name 😽	Size Type	Date Modified
Videos	觉 verdiLog	9 items folder	Wed Apr 24 18:44:21 201
▷ 📁 byf	/ core00.txt	12 bytes plain text document	Thu Jul 18 15:30:31 2013
	dma0.txt	0 bytes plain text document	Wed Jul 31 14:30:32 2013
v ⊃ jyz	dma1.txt	0 bytes plain text document	Wed Jul 31 14:30:32 2013
D DINCA libs	🧃 input.dat	2.5 KB MPEG video	Wed Jul 31 14:54:24 2013
▷ 📁 com_config	makeone_all_in_one_ver2.pl	6.9 KB Perl script	Wed Mar 21 18:59:56 201
▷ 📁 config	imulti_core_test.v	10.3 KB plain text document	Wed Jul 31 14:30:20 201:
▷ 📁 dma_test	ncverilog.key	5 bytes plain text document	Wed Jul 31 14:25:27 2013
D Dig	ncverilog.log	44.6 KB application log	Wed Jul 31 14:31:50 2013
▷ D lyh	🏐 offchip_mem.dat	1.3 MB MPEG video	Wed Jul 17 12:49:03 2013
	🏐 offchip_mem_bak.dat	25.0 KB MPEG video	Fri Jul 5 19:29:00 2013
▷ D core01	[] one_lcu.log	622 bytes application log	Tue Jul 2 23:30:19 2013
▷ 📁 core04	[] run.f	7.6 KB Fortran source code	Wed Apr 24 18:43:34 201
▷ 📁 core05	isim.bat	76 bytes plain text document	Wed Apr 24 18:43:35 201
D D Core06 🛛 💌	•	111	

13. 修改 testbench 中的 packet_num

·		net/de	llr900b/e	xport/ybf	an/jyz/	me_he	vc_32/mu	lti_core_t	est.v (modi	fied) - ge	dit			x
<u>F</u> ile <u>E</u> dit	<u>V</u> iew	Sear	ch <u>T</u> ool	ls <u>D</u> ocur	nents	<u>H</u> elp								
New	Den Den	¥	G Save	Print	1	∽Undo	Redo	Cut	Copy	Paste	Ĩ	S Find	Seplace	
🧭 c0_8x8.	c 🗙 😼	c1_8	(4.c ×	🧭 c6_4x8	3.c ×	🧭 c7.	_det.c ×	🛅 multi_	_core_test.v	* ×				
1`ti	mesca.	le 1	00ps	/ 100p	s									*
2														1
3 mod	ule mu	ulti,	_core	_test;										11141
4														
5 par	ameter	r pa	cket_	num =	512;									
6 par	ameter	r se	t_data	a =										
54'	60000	0000	0_000	000000	_000	0000	00_000	000000	_000000	0000_0	000	0000	0;	
7														
8			-											
91nt	eger (echo	2;											
11	2112.0													
12 reg	CIK_	000;												
12 reg	rstn.	_000	;											
14 rog	rstn.	_p_0	00;											
15 reg	retn	001												
16 reg	rstn	_001 n 0	, 01 ·											
17 reg	clk (10^{-10}	ο τ ,											
18 reg	rstn	010												
19 reg	rstn	p 0	10:											
20 reg	clk (011:												
21reg	rstn	_011												
						11					-			_ ×
						7.0								F

/net/dellr900b/export/ybfan/jyz/me_hevc_32/multi_core_test.	v (modified) - gedit
<u>File E</u> dit <u>V</u> iew <u>S</u> earch <u>T</u> ools <u>D</u> ocuments <u>H</u> elp	
New Open Save Print Undo Redo Cut	Copy Paste Find Replace
	e_test.v* ×
1`timescale 100ps / 100ps 2 3module multi_core_test; 4	
5parameter packet_num = 279;	
6parameter set_data = 54'b00000000_00000000_00000000_0000000_0	00000000_000000000;
o 9 junteger echo2:	
10	
11 reg clk_000; 12 reg rstn_000; 13 reg rstn_p_000; 14 reg clk_001;	
15 reg rstn_001;	
16reg rstn_p_001;	
17 reg clk_010;	
18 reg rstn_010;	
20 reg clk 011:	
21reg rstn_011;	
4	*

14. 在 telnet 中输入 run sim.bat,运行程序

						Termin	al						IX)
<u>F</u> ile	<u>E</u> dit	<u>V</u> iew	<u>T</u> erminal	Ta <u>b</u> s	<u>H</u> elp								
Edit n Ver dellr hevc_i dellr _ver2 dellr 279 dellr			— MG_LIB/m dellr900 dellr900 input.da dellr900	clude ti_mod b/expo b/expo t b/expo	the li lelsim_ ort/ybf ort/ybf ort/ybf	ne : verilo ^c an>[41 ^c an/jyz ^c an/jyz	g/libmg]cd /ne /me_hev /me_hev	gmm.so et/del] vc_32> vc_32> vc_32>	lr900b/ 42]per 43]wc (44]run	export, 1 make input. sim.b	/ybfan/ one_all dat at	jyz/me _in_on	ie III
													*

						Terminal	
<u>F</u> ile	<u>E</u> dit	<u>V</u> iew	<u>T</u> erminal	Ta <u>b</u> s	<u>H</u> elp		
count	:er =		3				*
count	er =		2				
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count	er =		2				
count	er =		2				
count	er =		1				
count	er =		1				
count	er =		1				
count	:er =		1				
@	240	010					
@	6850	090, m	iips0 ech	p1: 5	000500	00	×