

Do It Yourself

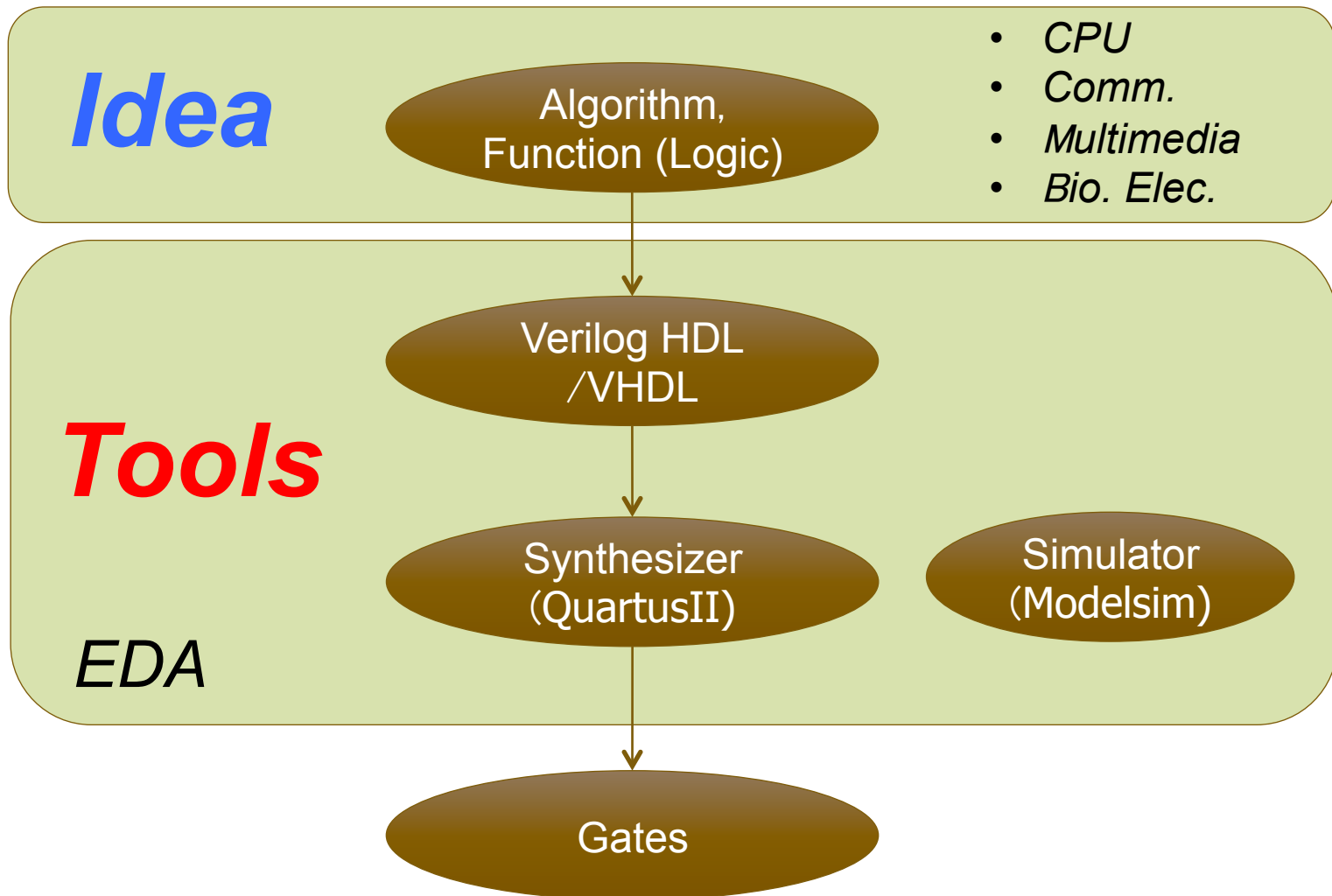
Verilog/Modelsim/QuartusII

Video Image Processing (VIP)
Research Group @ Fudan
<http://soc.fudan.edu.cn/vip/>

范益波

2013.9

DIY Basic



Software Download

Modelsim : HDL simulator

QuartusII : FPGA IDE (HDL synthesizer ...)

Altera Free Version:

<https://www.altera.com/download/sw/dnl-sw-index.jsp>

Resource from Altera

http://www.altera.com/education/univ/materials/digital_logic/tutorials/unv-tutorials.html

Educational Materials

- Dev. & Education Boards
- Software Tools
- Digital Logic
 - Tutorials
 - Laboratory Exercises
 - IP Cores
 - Design Examples
 - Required Tools
 - Textbooks
- Computer Organization
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- University Links

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Digital Logic Tutorials

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The provided tutorials enable students to design and implement circuits on Altera®'s DE-series boards using the Quartus® II software. They are easy to read and are intended for self-study.

The following table shows the available tutorials. Some tutorials exist in multiple versions, dependent on a hardware description language (Verilog or VHDL), a development board, and a Quartus II version. Use the filters below to choose the ones that are appropriate for your course.

Filter Materials

Choose HDL:

Choose Board:

Choose Quartus II Version:

Table 1. Digital Logic Tutorials for Quartus II 12.0

Title	Downloads
<i>Getting Started Tutorials</i>	
Getting Started with the DE-Series Boards	PDF
Quartus II Introduction Using Schematic Designs	PDF
Quartus II Introduction	PDF
Using the Library of Parameterized Modules (LPM)	PDF
<i>Timing Analysis Tutorials</i>	
Using TimeQuest Timing Analyzer	PDF
<i>Simulation Tutorials</i>	
Introduction to Quartus II Simulation	PDF
Introduction to ModelSim's Graphical Waveform Editor	PDF
Using ModelSim to Simulate Logic Circuits	PDF
<i>Debugging Tutorials</i>	
Signal Tap II Logic Analyzer	PDF
Debugging of Verilog Hardware Designs	PDF

Online Training

<http://www.altera.com/education/training/trn-index.jsp>

- **Modelsim**

http://www.altera.com/customertraining/webex/ModelSim/Overview_of_ModelSim.html

- **Quartus II Software Design Series: Foundation (Online Training)**

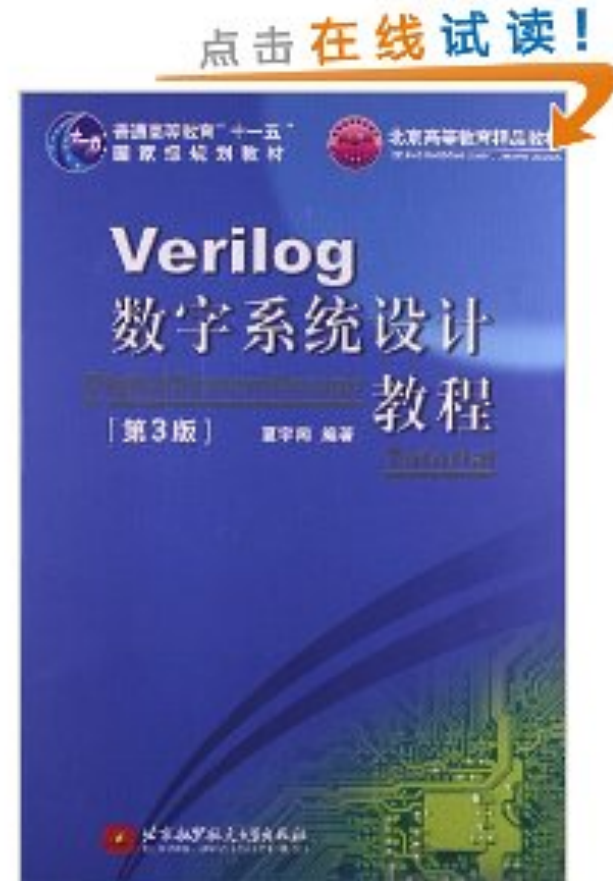
<http://www.altera.com/customertraining/webex/Foundation/launcher.html>

- **Verilog HDL Basic**

<http://www.altera.com/customertraining/webex/Verilog/player.html> (英文)

http://www.altera.com/customertraining/webex/Verilog_CN/player.html (中文)

Reference Book



Choose a start point

Altera Design Example

<http://www.altera.com/support/examples/exm-index.html>

Opencores

<http://opencores.org/projects>

VIP
AIB

Video Image Processing

Research Group @ Fudan

<http://soc.fudan.edu.cn/vip/>

Thank you !